## U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by **Komatsu Electronic Metals Co., Ltd.** 

(Insert ASSIGNEE's Name(s) Address(es))

25-1, Shinomiya 3-chome, Hiratsuka-shi, Kanagawa 254-0014 Japan

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

(Title of Invention)

Semiconductor wafer inspection device and method

(\*If the assignment is being filed after the filing of the application, this section must be completed)

for which application for Letters Pate	t of the	United	States	was	executed	on	even	date	herewith
unless otherwise indicated below:									

\* filed on \_\_\_\_\_ Serial No. \_\_\_\_

(Joseph P. Farrar is hereby authorized to insert the series code, serial number and/or filing date hereon, when known)

and all Letters Patent of the United States to be obtained therefore on said application or any continuation, divisional, substitute, reissue or reexamination thereof for the full term or terms for which the same may be granted.

The ASSIGNOR agrees to execute all papers necessary in connection with the application and any continuation, divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference, litigation, or other legal proceeding which may be declared concerning this application or any continuation, divisional, reissue or reexamination thereof or Letters Patent or reissue patent issued thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing evidence and proceeding with such interference, litigation, or other legal proceeding.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Signatures)

Funi habeshima	Fumi NABESHIMA	5/9/2006
(Signature)	(Type Name)	(Date)
Kanuya Tasashi	Kazuya TOGASHI	\$/9/2006
(Signature)	(Type Name)	(Date)
Hiroshi Jaken	Hiroshi JIKEN	4/9/2006
(Signature)	(Type Name)	(Date)
Joshinori Sugnage	Yoshinori SUENAGA	5/9/2006
Joshinovi Sulnage (Signature)	Yoshinori SUENAGA (Type Name)	5/9/2006 (Date)
(Signature)	(Type Name)	(Date)
(Signature)	***************************************	
(Signature)	(Type Name)	(Date)